

SIMCom_SIM800H_EAT_RVCT_Installation_User Manual_V1.01





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Version History

Data	Version	Description	Author
2013-11-27	V1.01	Created	Jack Sun



- 1. Copy folders **RVCT_EAT** and **FlexIm** to system root. *Please contact with SIMCom FAE team to get these files*;
- 2. Modify the license.dat in FlexIm;

Start->Run->cmd->ipconfig/all, physical Address to check the MAC address;

```
Physical Address. . . .
                                .: 60-EB-69-E8-73-B8
Dhop Enabled. . .
                                    Yes
Autoconfiguration Enabled
IP Address. . . . .
                                   172.21.2.46
Subnet Mask
                                    255.255.0.0
Default Gateway
                                    172.21.254.254
DHCP Server
                                    192.167.100.5
DNS Servers
                                    192.167.100.146
                                    192.167.100.147
                                   2013年11月27日 15:28:07
                                   2013年12月4日 15:28:07
Lease Expires
```

open the license.dat by Ultra-Edit or Text, find the *HOSTID*=00023F0468D4, there should be **19**, then replace 00023F0468D4 with your MAC address (mine is 60EB69E873B8), save it;

3. Configure the environment variables:

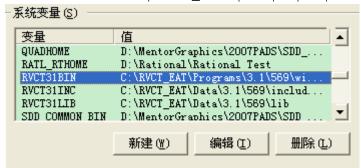
New three variables as follows:

Variables Value

RVCT31BIN C:\RVCT_EAT \Programs\3.1\569\win_32-pentium

RVCT31INC C:\RVCT_EAT \Data\3.1\569\include\windows

RVCT31LIB C:\RVCT_EAT \Data\3.1\569\lib



add one value in PATH:

Path C:\RVCT_EAT \Programs\3.1\569\win_32-pentium; as follows



after you finish all the above, type armcc in cmd, it will show as follows:



```
armcc [options] file1 file2 ... filen
Usage:
Main options:
               Generate ARM code
 arm
 -thumb
               Generate Thumb code
 -c90
               Switch to C mode (default for .c files)
               Switch to C++ mode (default for .cpp files)
 -срр
 00
               Minimum optimization
 01
               Restricted optimization for debugging
 02
               High optimization
 03
               Maximum optimization
               Optimize for codesize
 0space
               Optimize for maximum performance
 Otime
               Select CPU to generate code for (eg. ARM9E/ARM10E)
 -օրս Հօրս>
               Output a list of all the selectable CPUs
 -cpu list
o (file)
               Name the final output file of the compilation
               Compile only, do not link
C
               Output assembly code as well as object code
 -asm
               Output assembly code instead of object code
 S
               Interleave source with disassembly (use with --asm or -S)
 -interleave
               Preprocess the C source code only
 Ε
D(symbol>
               Define <symbol> on entry to the compiler
               Generate tables for high-level debugging
I(directory)
               Include (directory) on the #include search path
Software supplied by: mammoth//ZWTiSO 2005
```

Till now, the compile environment is finished;

4. Notice

Be noted that, the version of SIM800H's RVCT is **V3.1**; it is not compatible with SIM900's(V2.2), customer needs to do some changes for SIM900 (Delete Maybe if occur problem, the same as environment variables);



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